

Corres. and Mail DX AF



RESPONSE UNDER 37 C.F.R. § 1.116 EXPEDITED PROCEDURE REQUESTED **EXAMINING GROUP 2815** 

PATENT Customer No. 22,852 Attorney Docket No. 4329.2270-01

IN THE UNITED STATES PATENT AND TRADEMARK OFF

Group Art Unit: 2815

Examiner: Eugene Lee

In re Application of:

Shinsuke SAKAMOTO et al.

Application No.: 10/081,109

Filed: February 25, 2002

SEMIGONDUCTOR

INTEGRATED CIRCUIT DEVICE

AND WIRING ARRANGING METHOD THEREOF

Mail Stop AF

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

## AMENDMENT AFTER FINAL

In reply to the Final Office Action of May 2, 2003, and pursuant to 37 C.F.R.

§ 1.116, Applicants propose that this application be amended as follows:

## IN THE CLAIMS:

Please amend claim 1, as follows:

FINNEGAN HENDERSON FARABOW GARRETT & DUNNERLLP

1300 I Street, NW Washington, DC 20005 202.408.4000 Fax 202.408.4400 www.finnegan.com

1. (Amended Four Times) A semiconductor integrated circuit device,

comprising: